



Examining switch-debounce circuits

WHEN A SWITCH CLOSES, the contacts do not close instantly and finally. The contacts close, then bounce open, and this cycle repeats for a period, depending on the switch-manufacturing technology. The length of the bounce cycle is the "bounce time" (T_B).

You must eliminate switch bounce because each switch closure causes the output to change its voltage level, and the multiple switching sends a series of pulses to the remaining circuitry. A counter counts the bounces and flip-flops act crazy, so reliable operation requires the elimination of the bounces.

Switch-debounce circuits have been around for a long time, but I'm revisiting them because the art of designing them has skipped a generation of engineers. One example of a switch-debounce circuit puts a hysteresis gate after the switch, hoping that the gate's input hysteresis will pull the input up through the gate threshold. This battle is a losing one because the hysteresis, combined with the switch bounce, causes rail-to-rail switching. Another type of switch-debounce circuit uses a flip-flop after the switch, hoping that the flip-flop's feedback will cause it to change state on the first bounce. This circuit is subject to Murphy's law; it works 99% of the time and seems to fail only when management or customers are watching. Often, the first bounce has enough energy to get the flip-flop to change its output state, but the bounce lacks enough energy to meet the flip-flop's setup-time requirements. Thus, the flip-flop reverts to its original state.

Figure 1 works in all cases when you correctly design it. Before the switch closure, the capacitor charges to 5V, and the gate output is low. When the switch closes, the

capacitor discharges through R_2 until the voltage decreases enough to cause the gate to switch. Thus, the key to reliable operation is to ensure that the switch bounce stops before the gate reaches its input threshold voltage. Otherwise, the discharge time, R_2C , must be greater than the bounce time,

THE KEY TO RELIABLE OPERATION IS TO ENSURE THAT THE SWITCH BOUNCE STOPS BEFORE THE GATE REACHES ITS INPUT THRESHOLD VOLTAGE.

T_B . This example uses a CMOS hysteresis gate with a threshold voltage of 0.45 to 0.55 times V_{CC} . The discharge equation is:

$$R_2C = -\frac{T_B}{\ln \frac{V_{th}}{V_{CC}}},$$

where $T_B=1$ msec, $V_{th}=0.55V_{CC}$, $R_2C=1.67(10^{-3})$, $C=0.1$ μ F, and $R_2=16.7$ k Ω .

The measured time until the gate reaches its threshold voltage is a bit longer than you might predict because the switch bounce prolongs the capacitor discharge time when it opens the circuit. R_2 must sink the input leakage or the low-level input current that flows out of the gate. In this CMOS example, the gate input current is on the order of 1 μ A, so the final low-level input voltage is $16.7(10^3)(10^{-6})=16.7$ mV. If this were a junction transistor gate, the input current would be in the milliamp range. You use a hysteresis

gate because the R_2C filter causes the gate voltage to have a slow fall time. Slow rise or fall times keep the gate in its low-noise-immunity-threshold region for a long time, so you need hysteresis to pull the input out of the threshold the first time the gate switches.

When the switch opens, C charges through R_1 and the diode. You can select a small value for R_1 because switches do not tend to bounce when they open; the switch manufacturer or measurements must verify the bounce time and whether there is no bounce on opening. Selecting R_1C as a small time constant enables a fast recovery time; this quick recovery time

helps prevent malfunction when an operator actuates the switch multiple times. You size R by the amount of idle current you are willing to sacrifice. □

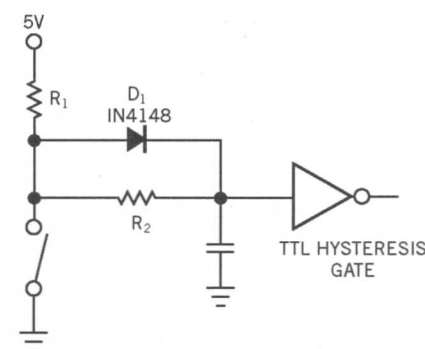


Figure 1

Adding a few passive components to a hysteresis gate makes a reliable switch-debounce circuit.

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